

FIG.1(a)

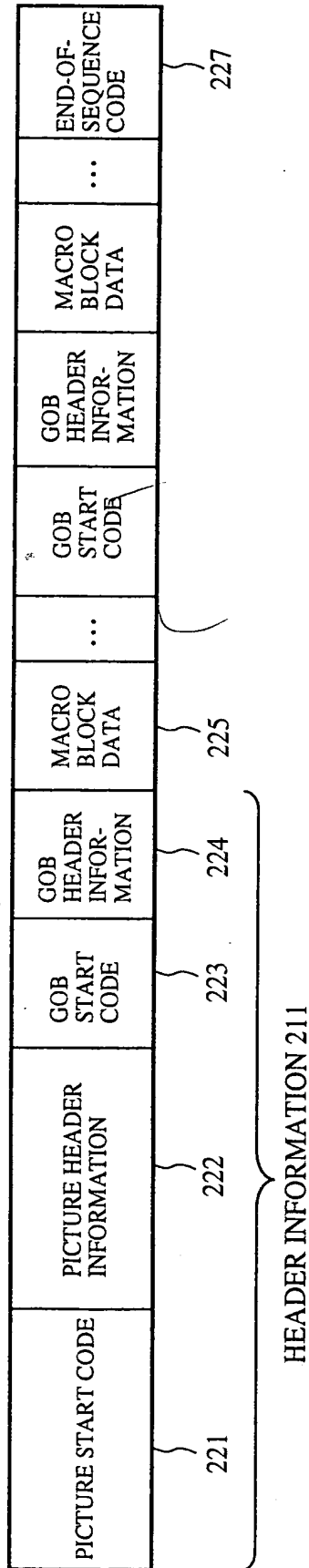
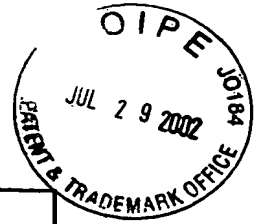
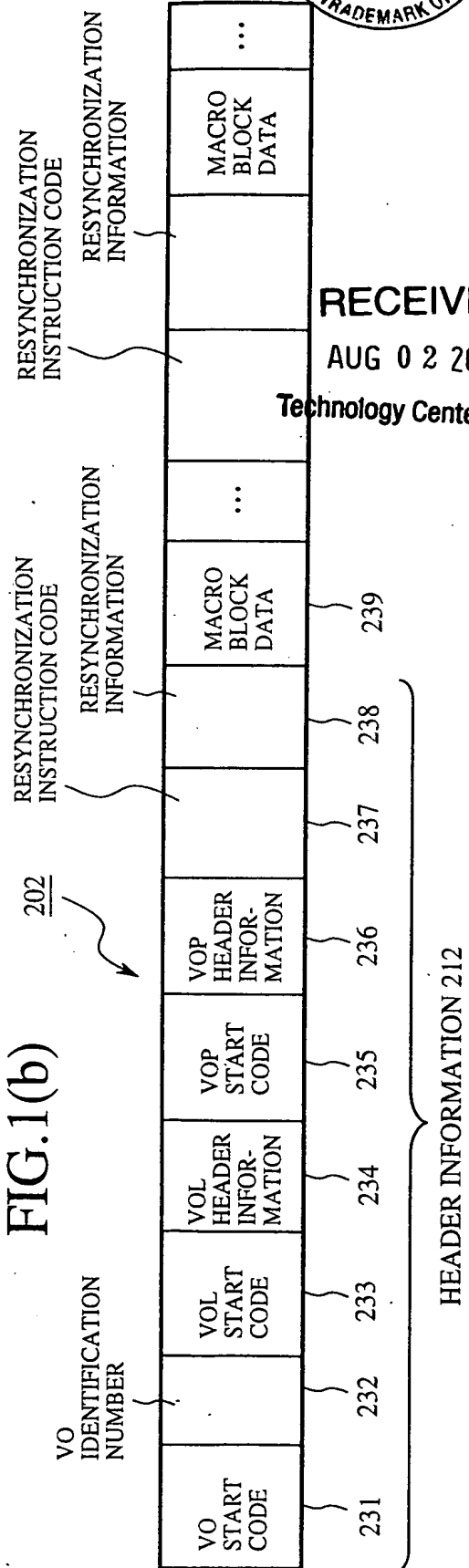


FIG.1(b)



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VO START CODE 231



Diagram illustrating the structure of a packet, showing fields and their corresponding bit positions:

- VO IDENTIFICATION NUMBER** (bits 231-233)
- H.263 COMPATIBLE IDENTIFICATION INFORMATION** (bits 234-236)
- RESYNCHRONIZATION INSTRUCTION CODE** (bits 237-239)
 - RESYNCHRONIZATION INFORMATION** (bits 237-238)
 - MACRO BLOCK DATA** (bits 239)

Additional fields shown in the diagram include:

- VO START CODE** (bit 231)
- VOL START CODE** (bit 233)
- VOL HEADER INFORMATION** (bit 234)
- VOP START CODE** (bit 235)
- VOP HEADER INFORMATION** (bit 236)
- MACRO BLOCK DATA** (bits 239)

The diagram also includes a **RECEIVED** stamp dated **AUG 02 2002** from **Technology Center 2600**.

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FIG.3

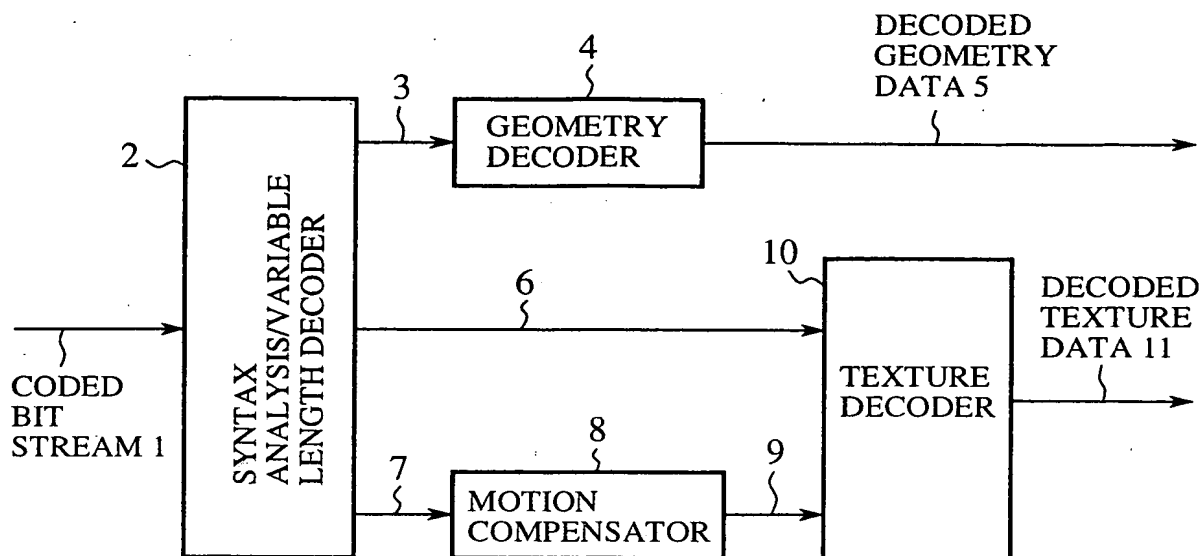
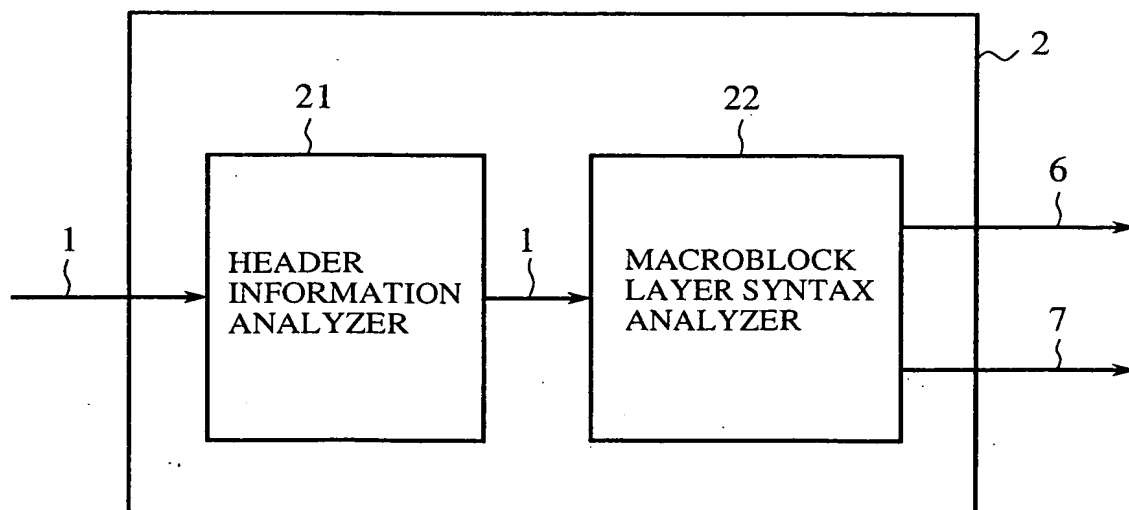


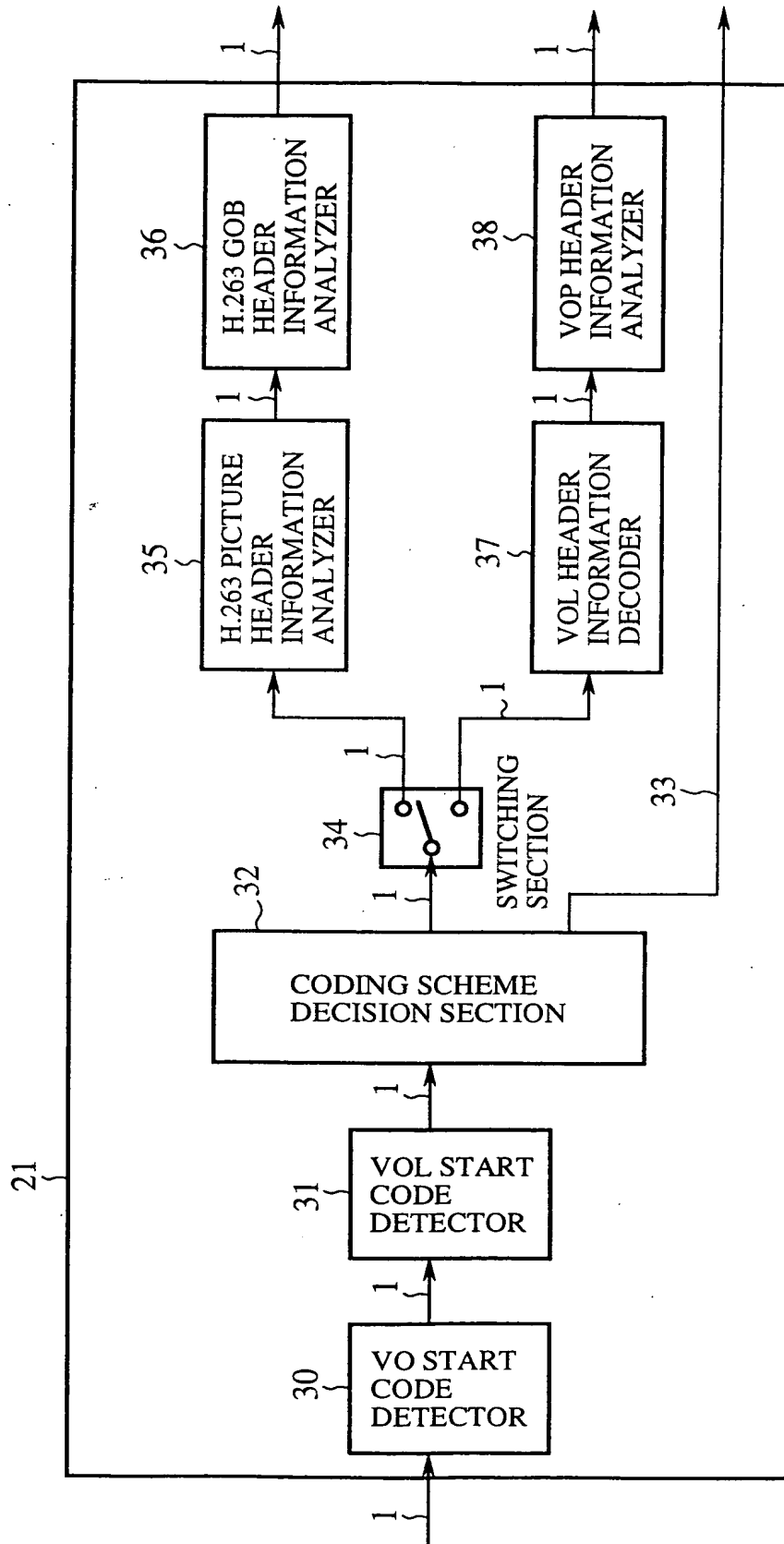
FIG.4



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FIG.5



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FIG.6

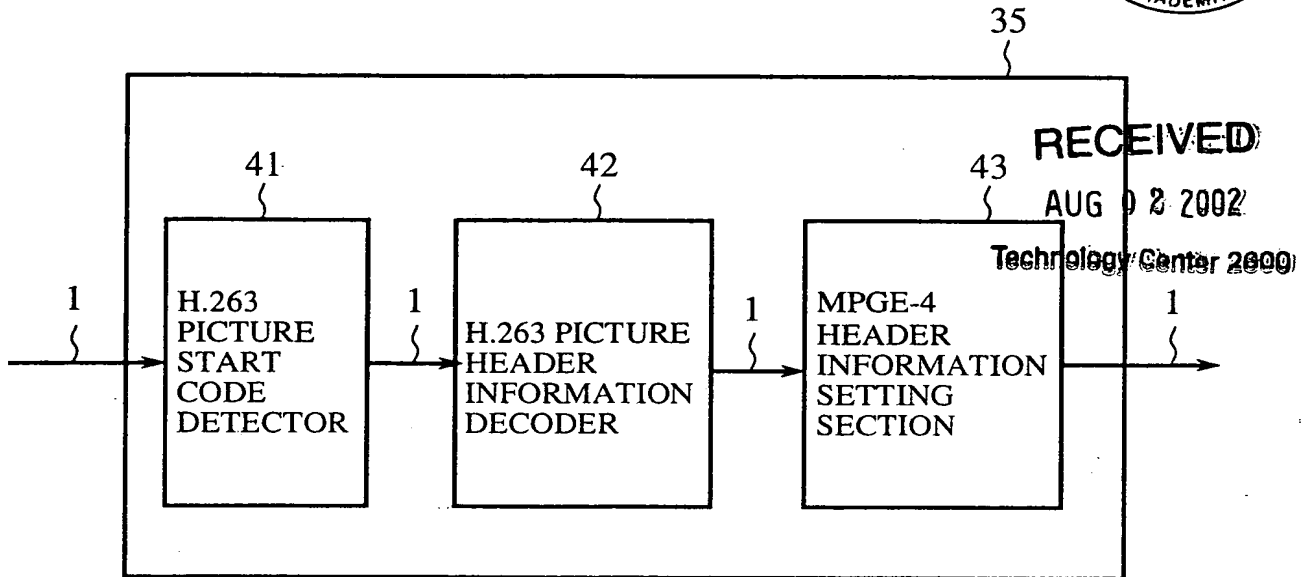
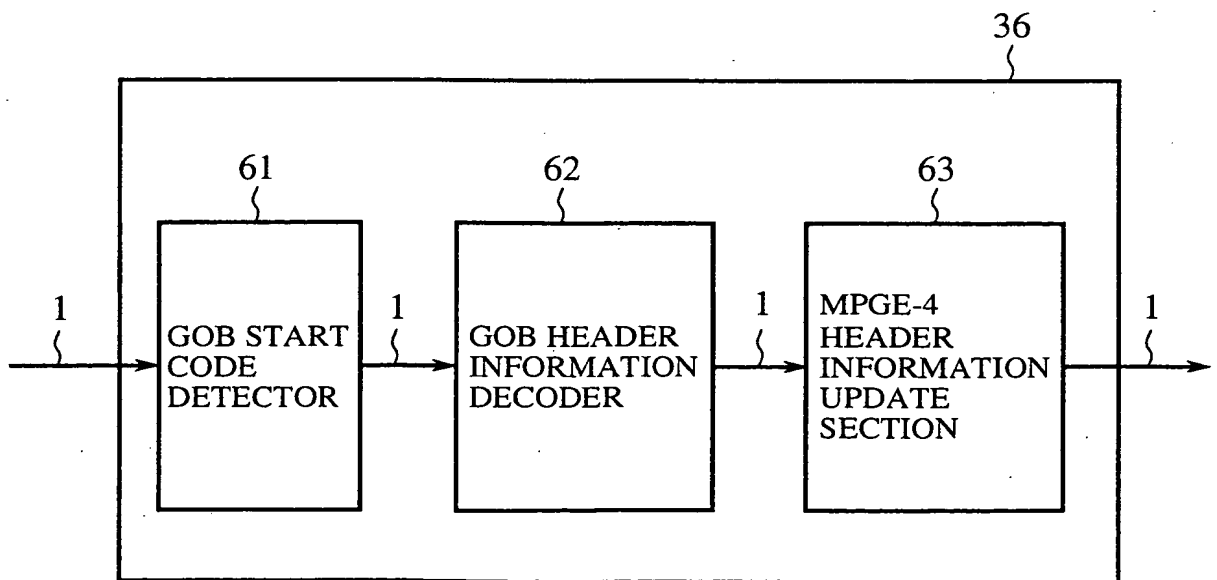


FIG.8



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FIG. 7

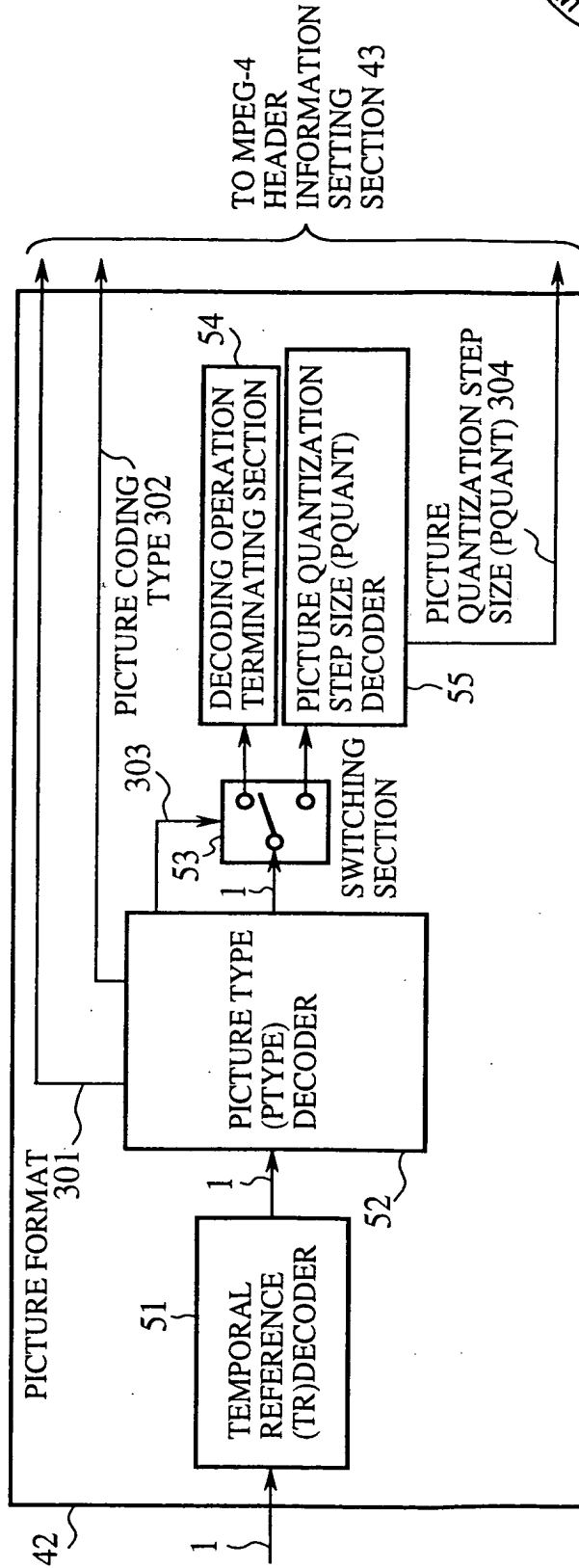
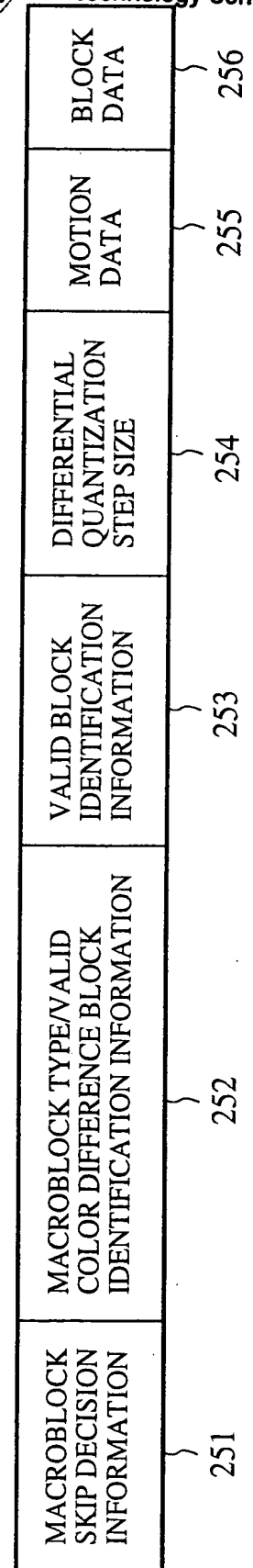


FIG. 11



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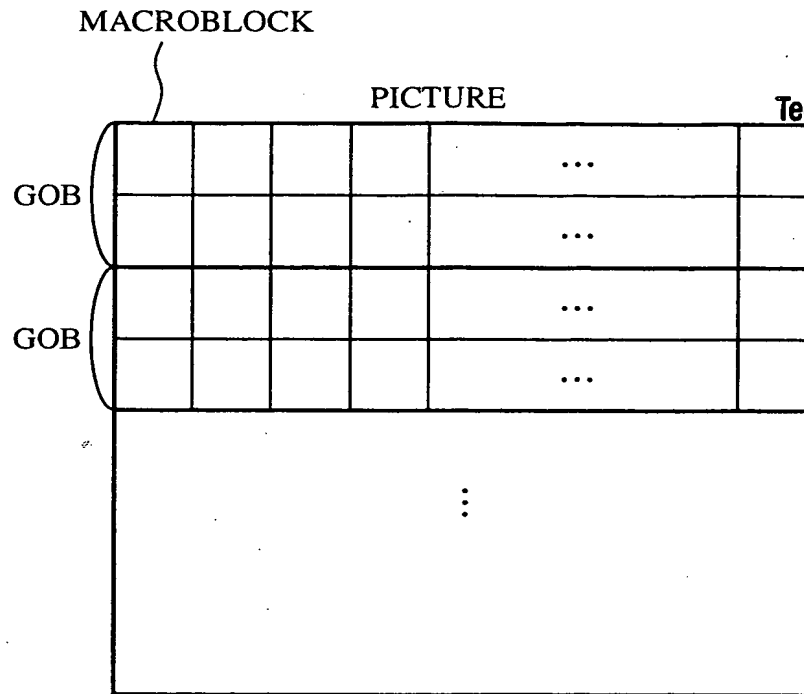
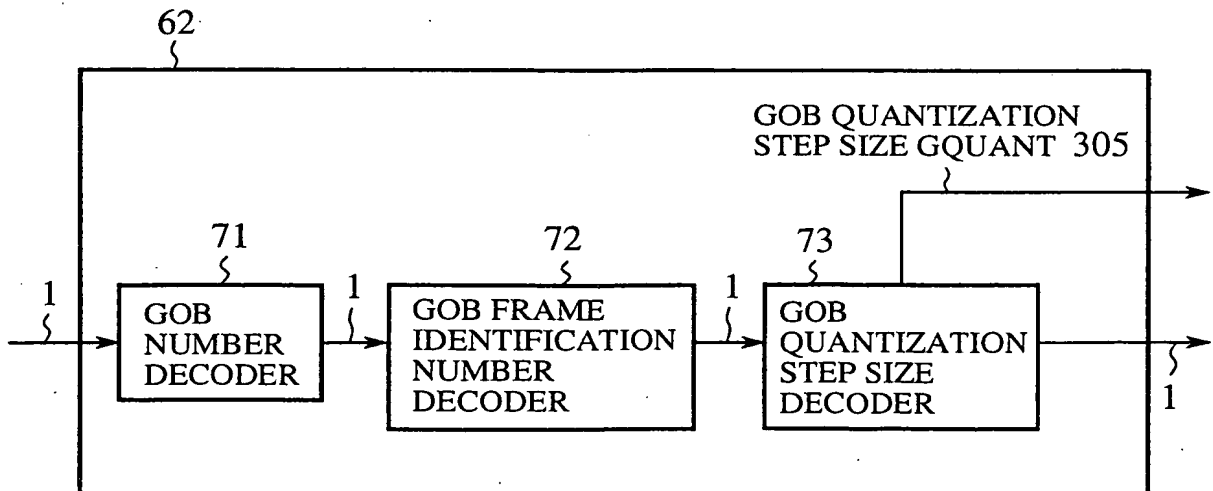


FIG.10



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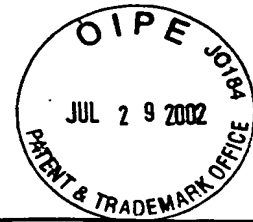
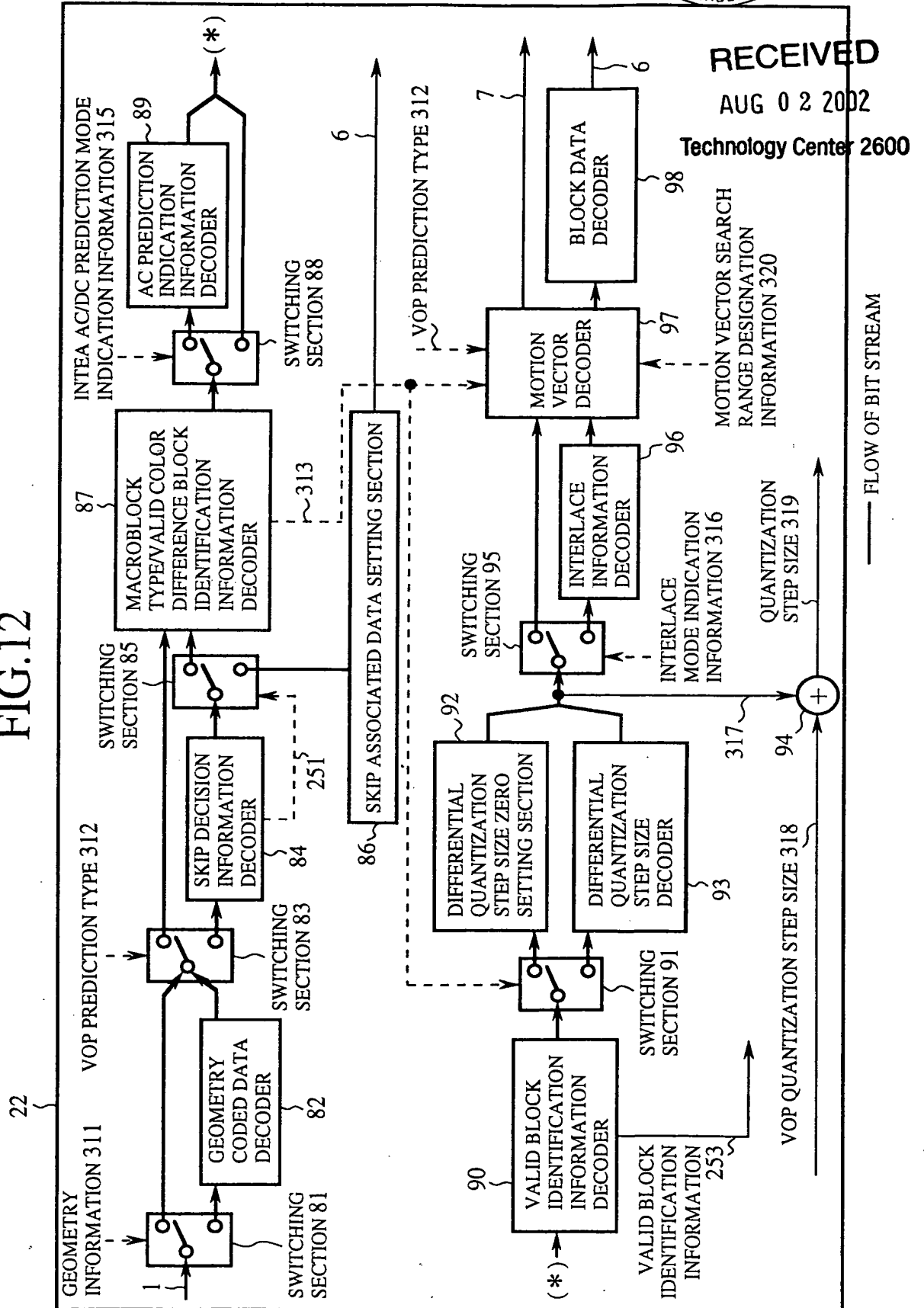


FIG.12



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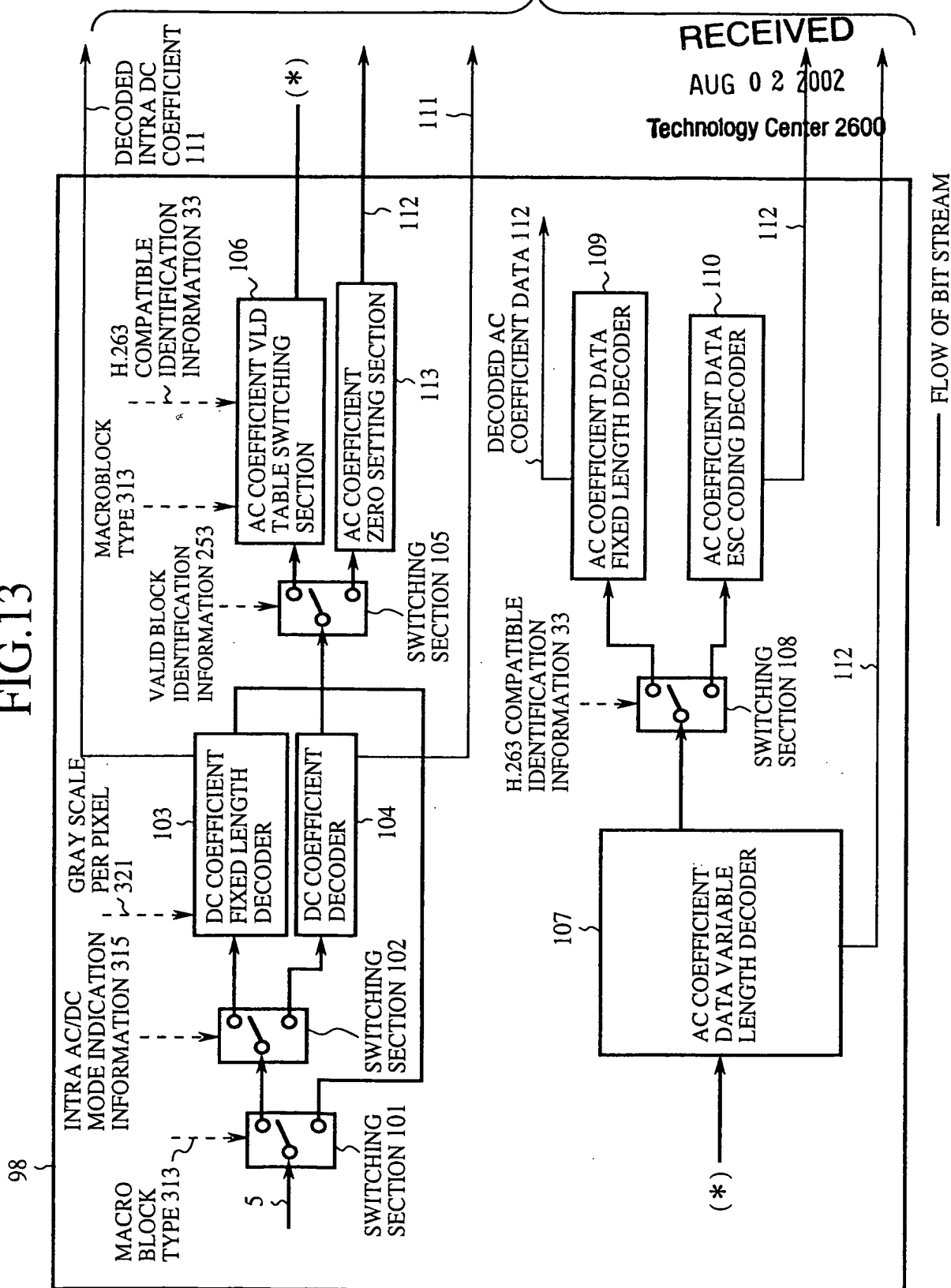
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TO 6 OF FIG.2

FIG.13



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— FLOW OF BIT STREAM

FIG.14(a)

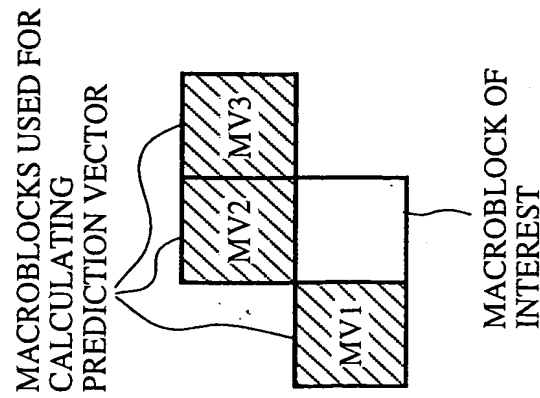


FIG.14(b)

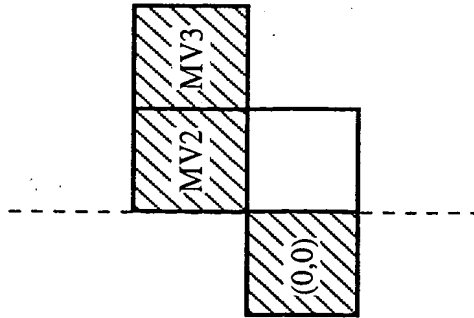


FIG.14(c)

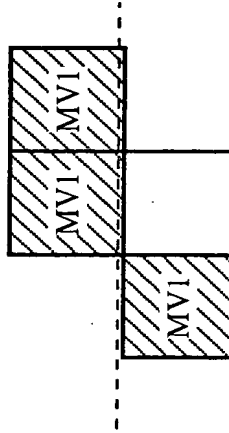
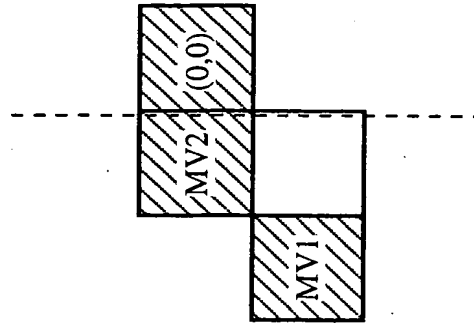


FIG.14(d)



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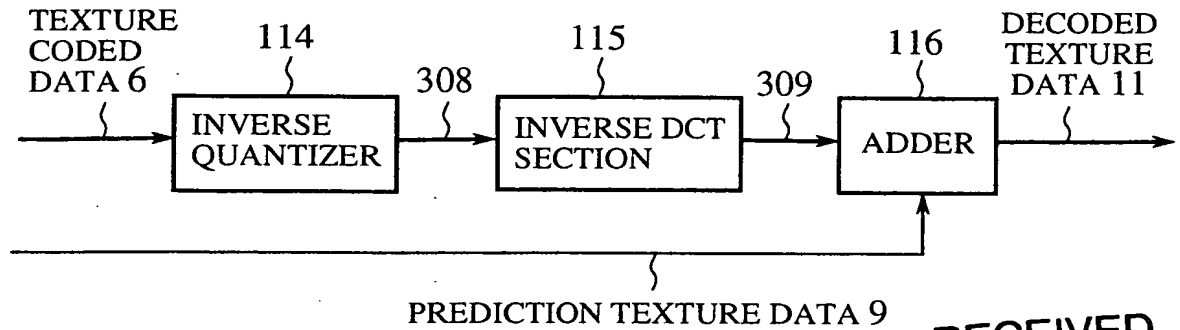
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----- VOP BOUNDARY (GOB BOUNDARY WHEN DETECTING GOB HEADER)

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FIG.15



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FIG.17

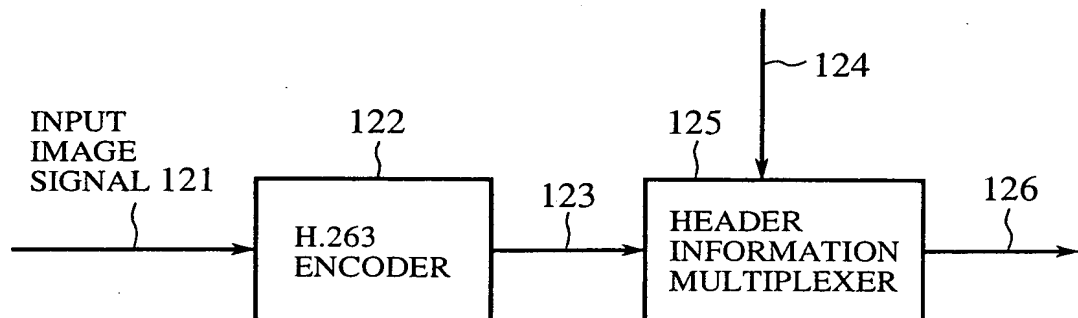


FIG.18

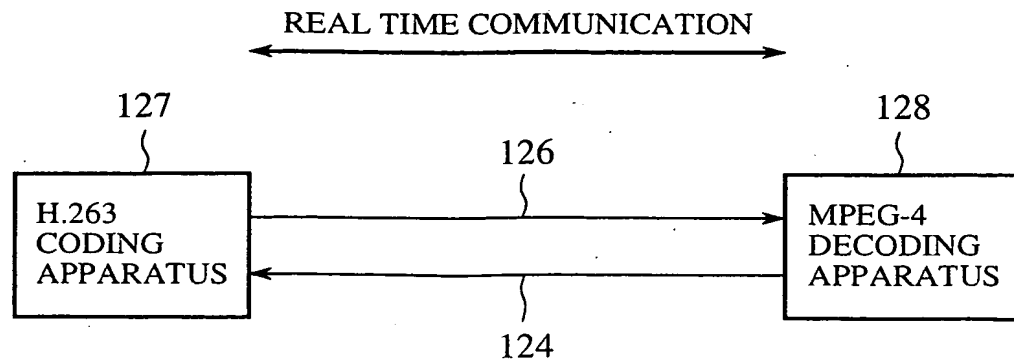
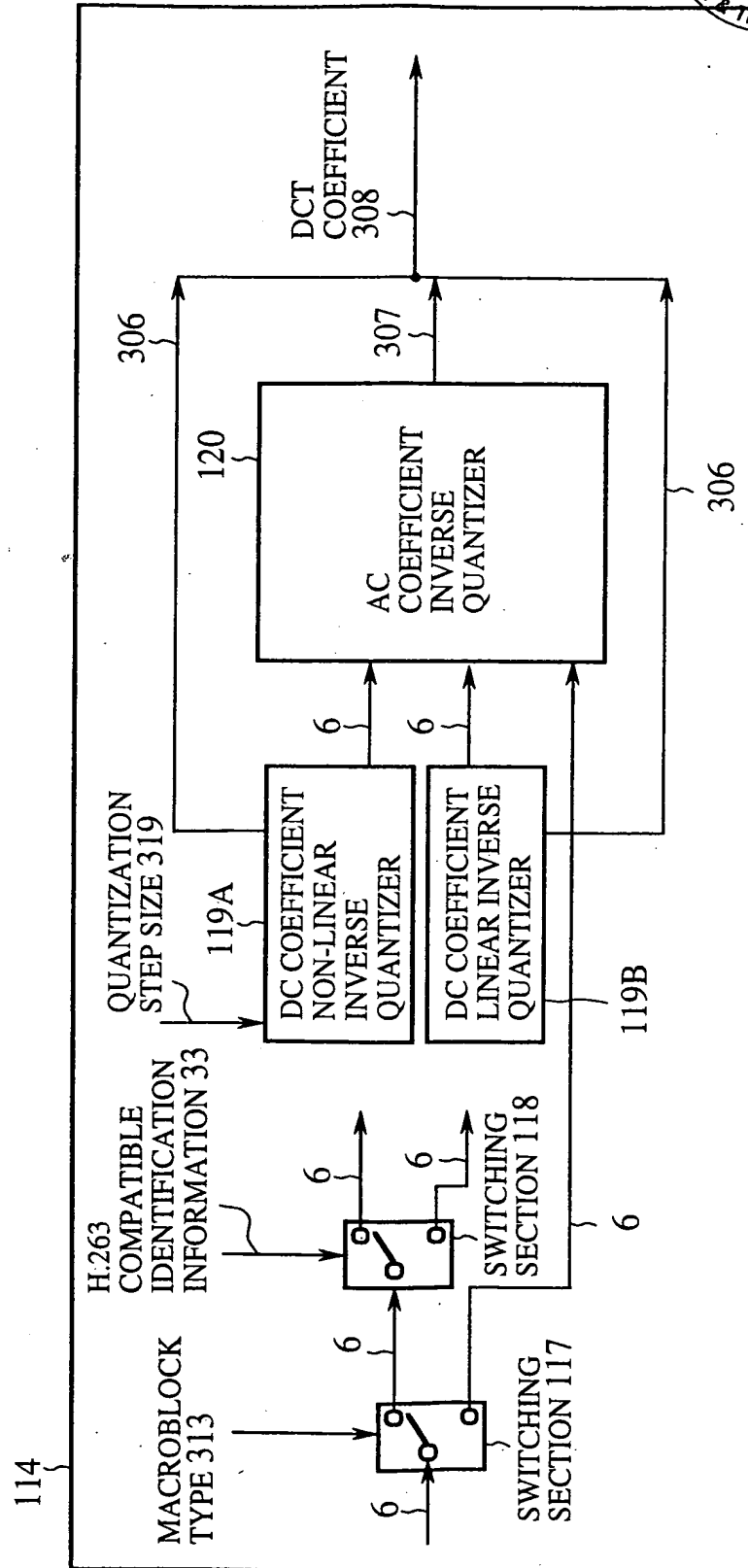


FIG.16



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FIG.19

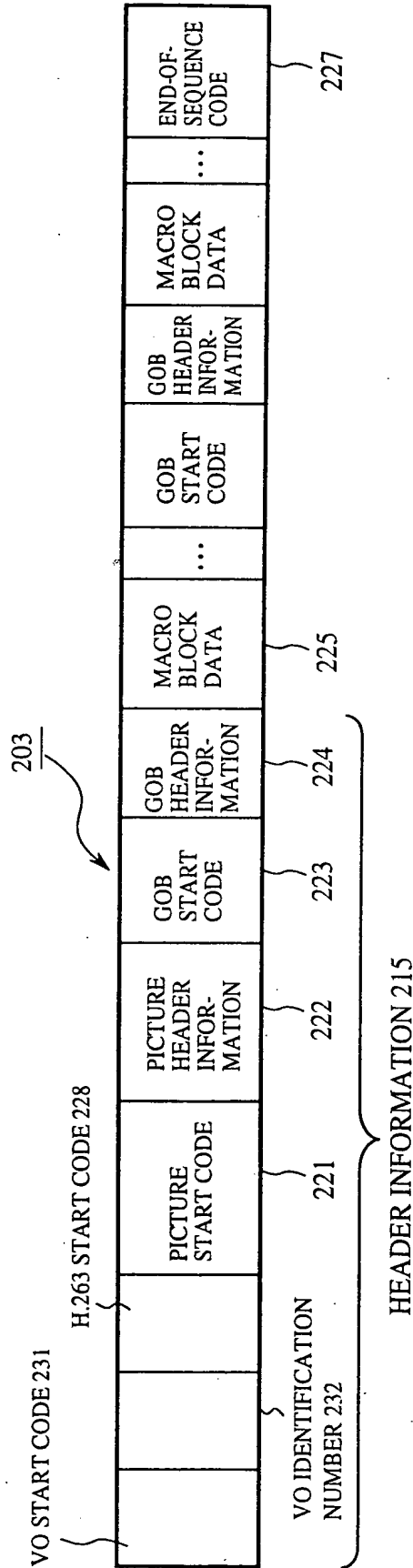


FIG.20

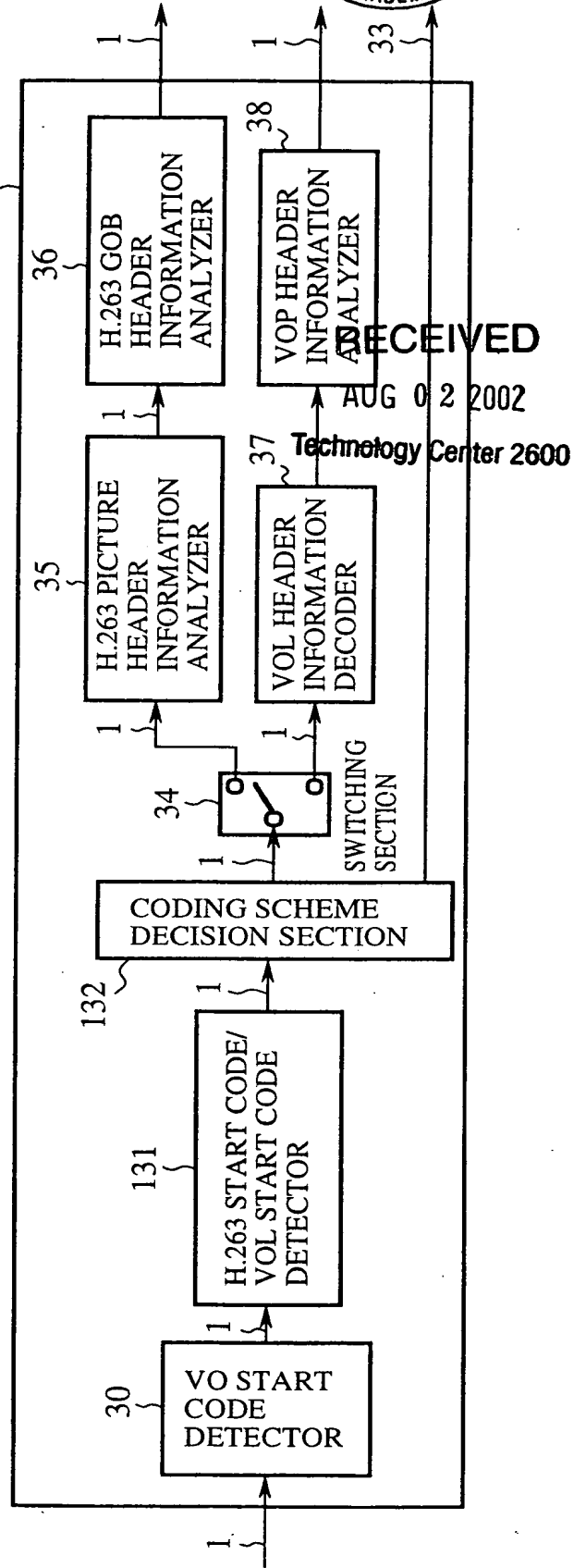




FIG.21

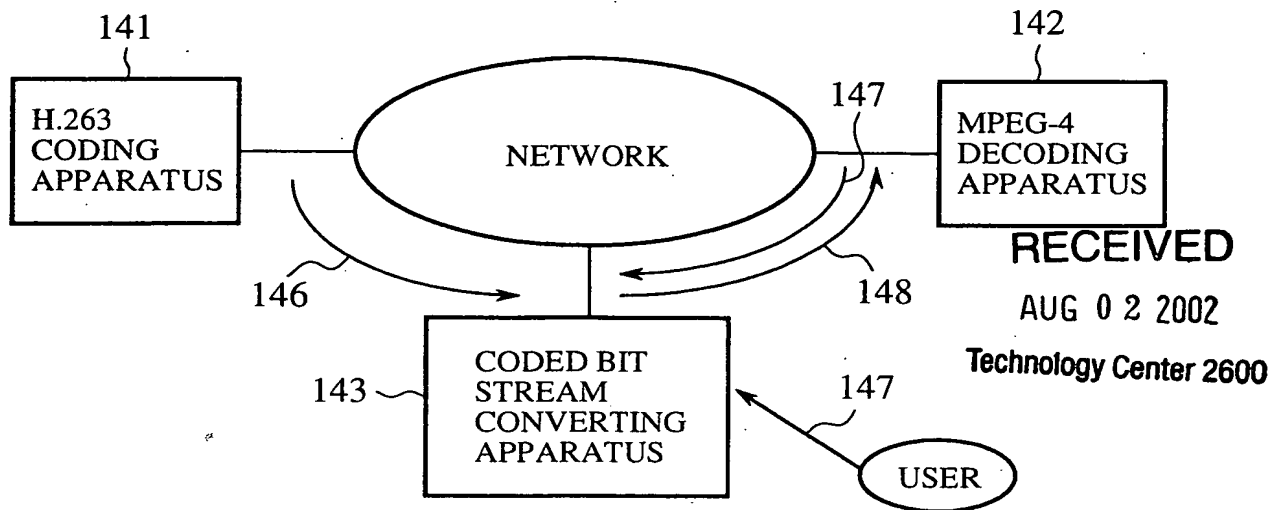


FIG.22

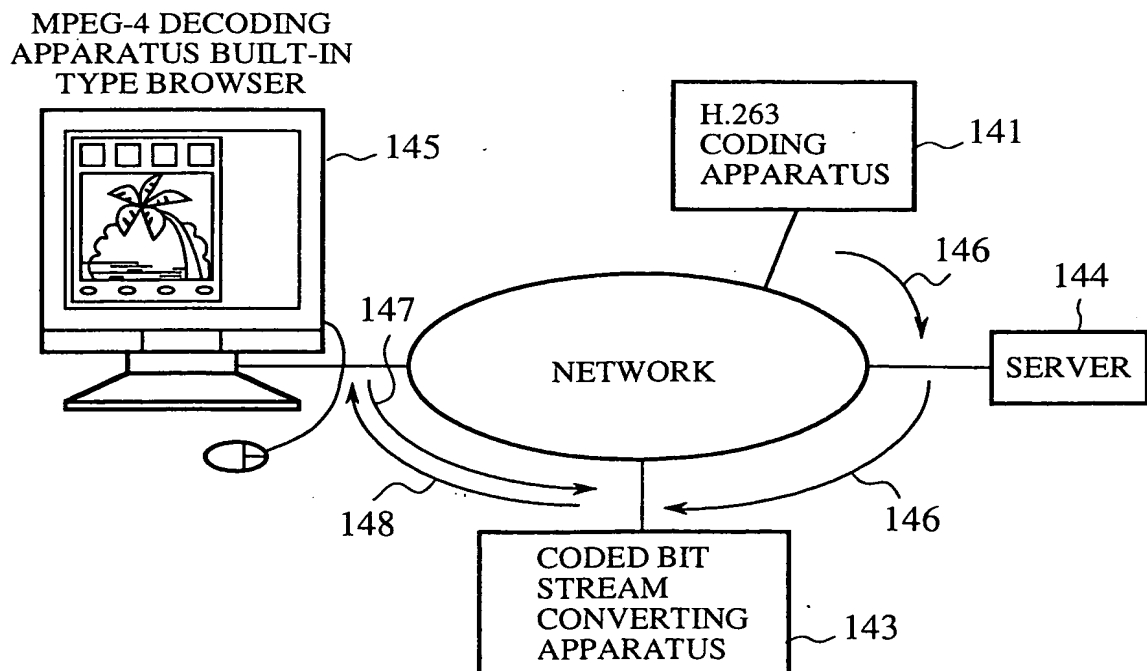


FIG. 23

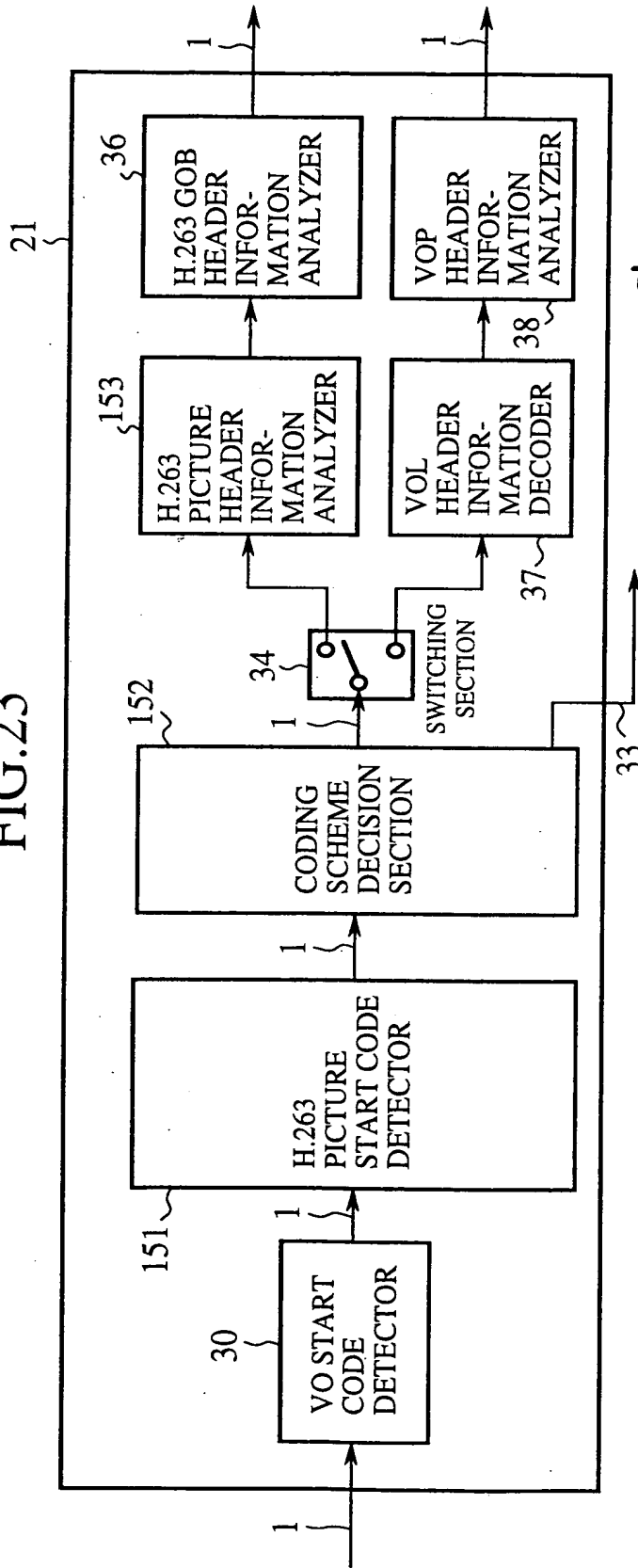
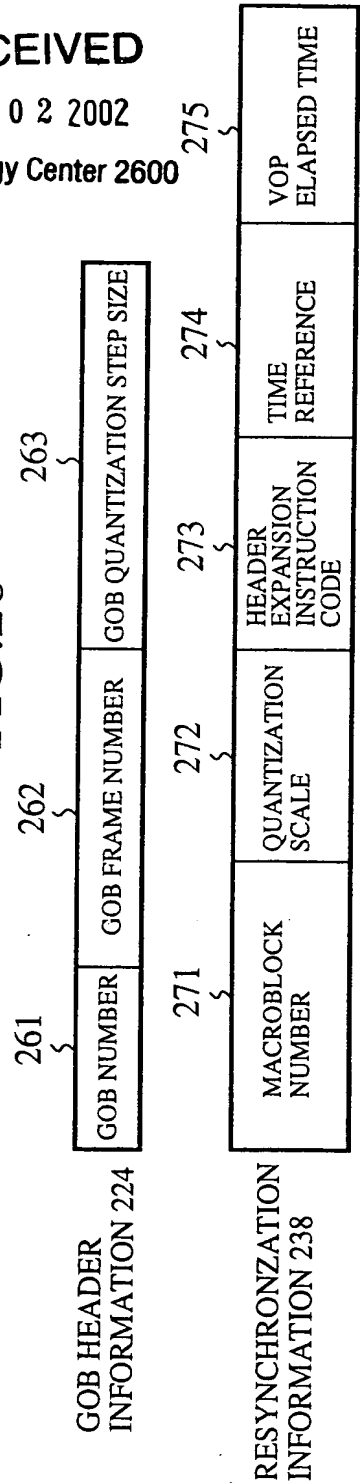


FIG. 26



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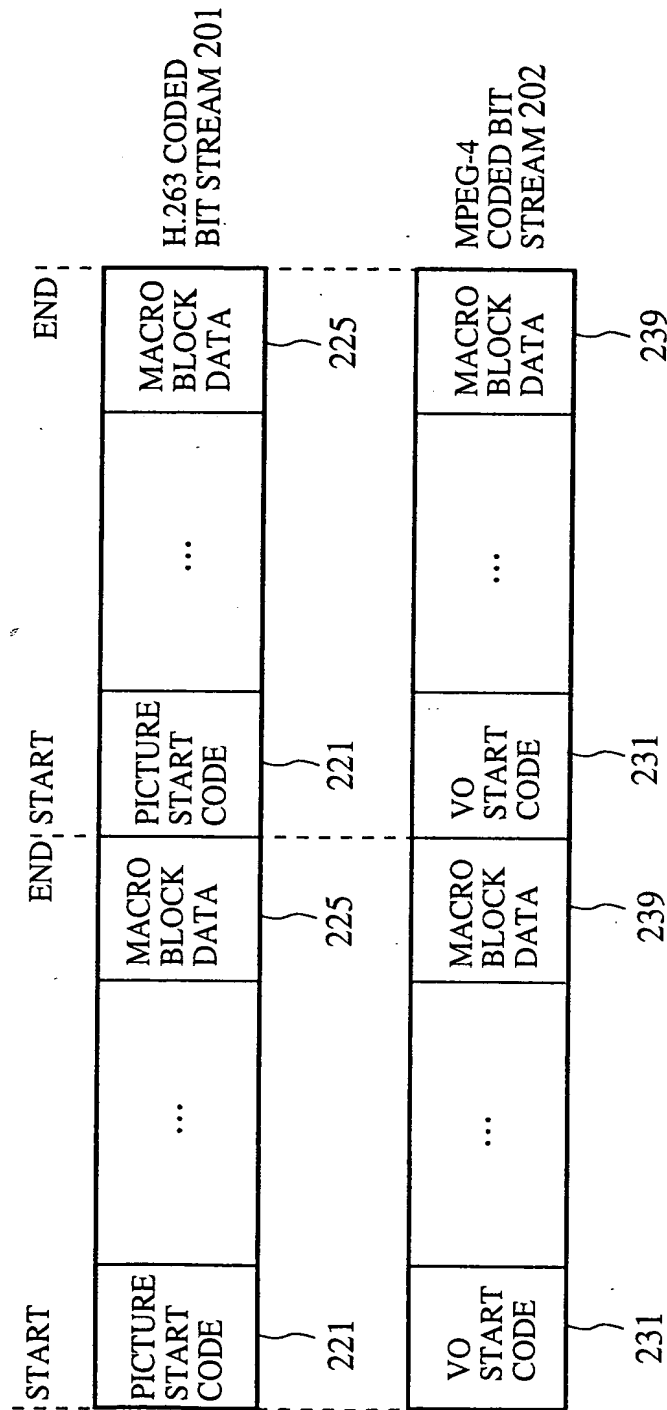


FIG.24(a)

FIG.24(b)

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FIG. 25

